

CONNOR HUMISTON

ELECTRICAL & COMPUTER ENGINEER

INFO

PHONE

720-626-8913

EMAIL

connor.humiston@colorado.edu

LINKS

ConnorHumiston.com

[Github.com/Connorado9](https://github.com/Connorado9)

[LinkedIn/Connor-Humiston](https://www.linkedin.com/in/Connor-Humiston)

SKILLS

PCB Design & Fabrication

Signal & Power Integrity

FPGA, VLSI & RTL Design

IC Design

Circuit Design & Assembly

Power Electronics/Converters

Algorithms & Data Structures

Embedded Systems

SOFTWARE

Altium Designer

Quartus Prime & ModelSim

ADS, HyperLynx SI PI

LANGUAGES

Python

C & C++

Verilog HDL

Assembly

Java

Tcl

Perl

Spanish

EDUCATION

B.S. in Electrical & Computer Engineering

University of Colorado
Boulder

August 2018 — May 2022

Minors: Computer Science, Spanish, Business, and TAM (Technology, Arts, and Media)

- **GPA 3.92;** Dean's List, Boettcher Presidential Scholar, Sewall Esteemed Scholar
- **Organizations:** Global Engineering, IEEE, CU Hyperloop, Rotaract, Tau Beta Pi, Alpha Phi Omega, Theta Tau (Vice President)
- **Classwork:** Circuits, Analog & Digital Design, PCB Design, Digital Logic, FPGAs, Signal & Power Integrity, Data Structures, Computer/CPU Architecture, Signals & Systems, Microelectronics, EM Fields & Waves, Power Electronics, IC Design

EXPERIENCE

National Institute for Standards & Technology (NIST)

Electrical Intern

March 2020 — Present

Drafted and assembled high-frequency oscillator circuits to measure noise for precision applications, and produced Python Uncertainty Propagation software

Amazon Web Services (AWS); Contracted

Hardware Engineer

August 2020 — May 2021

Designed and fabricated a self-identifying, wireless energy meter offering a scalable, economical solution for AWS's existing outdated deployments featuring GPS & Wi-Fi

PROJECTS

Printed Circuit Board Design

Altium, ADS

Produced high-performance Arduino Uno, low-phase noise sequenced regulators, energy meter, and research boards to study topologies, terminations & discontinuities

VGA Video Controller

Verilog

Mini-CPU, Reaction Timer

Verilog

Implemented digital circuits like clock dividers, comparators, counters, multiplexers, adders, encoders, LFSRs & PLLs in Verilog before verifying results in ModelSim

Uncertainty Propagation Software, Tic Tac Toe AI

Python

Zombie Invasion Simulator

C++

Utilized fundamental data structures like graphs, trees & linked lists with Depth- and Breadth-First Searches, Dijkstra's algorithm, and sortation to simulate an outbreak

Word Analysis, Transmission Network & Database

C++

Temperature-Based Embedded System

C

Developed LETIMER, CMU, GPIO, I2C, UART, BLE and scheduler drivers to collect temperature sensor readings and send them via Bluetooth to a mobile device

Remote-Controlled Robot

Custom Hardware

Check out ConnorHumiston.com for descriptions of these projects and much more!